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Serial No. 10/618,041

Application of: Lee A. Burton

Filed: July 11, 2003

Art Unit:

Examiner:

Attorney Docket No. SRC027

For: SWITCH/NETWORK ADAPTER PORT INCORPORATING SHARED MEMORY  
RESOURCES SELECTIVELY ACCESSIBLE BY A DIRECT EXECUTION LOGIC  
ELEMENT AND ONE OR MORE DENSE LOGIC DEVICES

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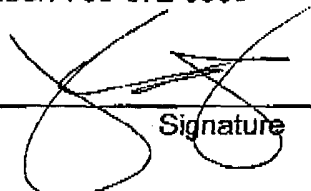
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In re Application of:

Lee A. Burton

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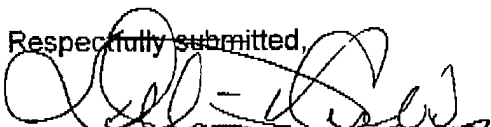
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This Information Disclosure Statement is filed before mailing of a first Office Action in the above case. Accordingly, no fee is believed due. However, any fee associated herewith may be charged to Deposit Account No. 50-1123.

Date

12 December 2003

Respectfully submitted,

  
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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				Application Number	10/618,041
				Filing Date	July 11, 2003
				First Named Inventor	Lee A. Burton
				Art Unit	
				Examiner Name	
Sheet	1	of	5	Attorney Docket No.	SRC027

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Document No. No. - Kind Code <sup>2</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Doc	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US-5,903,771	05/11/1999	Sgro et al.	Figs 1 & 6, col. 3, lines 30-67, col 4, lines 1-51, col 7, lines 1-27.	
		US-6,192,439	02/20/2001	Grunewald et al.	Fig 3, col 3, lines 53-67, col 4, lines 1-64.	
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		US-6,230,057	07/20/1993	Shido, et al.		
		US-5,892,962	04/06/1999	Cloutier		
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FOREIGN PATENT DOCUMENTS						
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Sheet	2	of	5	Attorney Docket No.	SRC027

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		AGARWAL, A., et al., "The Raw Compiler Project", pages 1-12, <a href="http://cag-www.lcs.mit.edu/raw">http://cag-www.lcs.mit.edu/raw</a> , Proceedings of the Second SUIF Compiler Workshop, Augs. 21-23, 1997.	
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		MORLEY, ROBERT E., Jr., et al., "A Massively Parallel Systolic Array Processor System", © 1988 IEEE, Publ. No. CH2603-9/88/0000/0217, Pages 217-225.		
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		YAMAUCHI, TSUKASA, et al., "SOP: A reconfigurable massively parallel system and its control-data flow based compiling method", @ 1996 IEEE, Publ. No. 0-8186-7548-9/96, Pages 148-156.	
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